

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	41134	card near3 type	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 10:22
L2	909367	(power voltage)near4(determin\$ identif\$ recogni\$ detect\$ select\$5 switch\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 14:10
L3	195	1 with 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 10:28
L4	184	3 and(@ad<"20031212" @prad<"20031212" @rlad<"20031212")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 11:20
L5	396529	((power pwr voltage)near3(good ok normal)) pgood pwrgood pwrok((power voltage)near7(stabl\$ stabili\$ fluctu\$ settl\$ equilibrat\$ normal\$ delay\$))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 11:19
L6	48	4 and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 10:35
L7	1081435	(power voltage)near4(enabl\$ supplied supplying provid\$ applied apply\$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 10:58
L8	92843	5 same 7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 11:00
L9	395	1 same 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 11:00
L10	28	(8 and 9)not 6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 11:14

L11	92140	5 with(delay\$ period predetermined)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 11:15
L12	21464	7 same 11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 11:15
L13	9	9 and 12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 11:16
L14	88	((((power pwr voltage)near3(good ok normal)) pgood pwrgood pwrok)near2 signal)near5 delay\$	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 15:28
L15	85	14 and(@ad<"20031212" @prad<"20031212" @rlad<"20031212")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 15:23
L16	532	((SRINIVASAN near2 KARTHIGAN)(MILLER near2 JOSEPH)).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 11:26
L17	3	1 and 2 and 16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 14:12
L19	2	"6675251".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 15:24
L20	948	((((power pwr voltage)adj(good ok normal)) pgood pwrgood powergood pwrok)near2 signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 15:30
L21	9	9 and 20	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 15:46

L22	2	"5613130".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 15:48
L23	2	"20020087906"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 15:49
S1	6778	(card adapter slot extension extender)with((power voltage)near2(rail type level requir\$))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 17:17
S2	61775	((card adapter slot extension extender)near2(type))((power voltage)near2(rail type level requir\$))near5(determin\$ identif\$ recogni\$ detect\$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 17:19
S3	79231	((power pwr voltage)near3(good ok normal)) pgood pwrgood	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 10:31
S4	722	S1 same S2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 15:47
S5	57	S3 and S4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 15:47
S6	168	(S1 and S2 and S3)not S5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 16:46
S7	164	S6 and(@ad<"20031212" @prad<"20031212" @rlad<"20031212")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 18:33
S8	5548	S3 same(delay\$ debounc\$ settl\$ timing timer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 16:49

S9	22	S7 and S8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 16:49
S10	11294	(card adapter slot extension extender expansion)with((power voltage)near2(rail type level requir\$ operati\$))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 18:27
S11	8261	(card adapter slot extension extender expansion board)near4(type ((power voltage)near2(rail level requir\$ operati\$)))near5(determin\$ identif\$ recogni\$ detect\$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 18:28
S12	121196	((power pwr voltage)near5(good ok normal stabl\$)) pgood pwrgood reset)same(delay\$ debounc\$ settl\$ timing timer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 17:33
S13	89580	(card adapter slot extension extender expansion board)with((power voltage)near4(switch\$3 suppl\$5 provid\$ appl\$5 control\$4 select\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 18:32
S14	227	S11 and S12 and S13	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 17:38
S15	203	(S14 and(@ad<"20031212" @prad<"20031212" @rlad<"20031212"))not(S5 S7 S9)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 10:28
S16	72753	(card adapter extension expansion board)near2 type	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 18:27
S17	909367	(power voltage)near4(determin\$ identif\$ recogni\$ detect\$ select\$5 switch\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 10:23
S18	269	S16 with S17	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 19:36

S19	256	S18 and(@ad<"20031212" @prad<"20031212" @rlad<"20031212")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 18:34
S20	58	S19 and("713"/\$3 "710"/\$3 "235"/\$6 "711"/\$3).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 19:32
S21	17	S12 and S19	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 19:32
S22	619	S16 same S17	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 19:36
S23	29	(S12 and S22 and(@ad<"20031212" @prad<"20031212" @rlad<"20031212"))not(S5 S7 S9 S20 S21)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 19:44
S24	16	S3 and S19	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 19:46